

Notice of References Cited	Application/Control No. 09/974,705	Applicant(s)/Patent Under Reexamination MACCHETTI ET AL.	
	Examiner Carl Colin	Art Unit 2136	Page 1 of 1

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*	U	Mc Loone, Maire; McCanny, John V; "Rijndael FPGA Implementation Utilizing Look-Up Tables"; 26-28 Sept. 2001; Signal Processing Systems, 2001 IEEE Workshop; Page(s): 349 - 360.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.